

IN THE CLAIMS:

Please accept amended claim 1 as follows.

1. (currently amended) A liquid crystal display device comprising:

a first substrate;

a second substrate being opposite to the first substrate;

a first color filter formed on a first portion of the second substrate, the first portion corresponding to a first area of a display area, the first area being a border area of the display area;

a second color filter formed on a second portion of the second substrate, the second portion corresponding to a second area of the display area, the second area being the display area except the border area;

a first pixel electrode disposed in the first area;

a thin film transistor which controls a voltage of the first pixel electrode;

a first liquid crystal layer disposed between the first and the second substrate in the first area, and a zero electric field being formed on the first liquid crystal layer; and

a second liquid crystal layer disposed between the first and the second substrate in the second area, wherein a gate electrode of the thin film transistor is connected to ground so that a data line is electrically disconnected from the first pixel electrode, and wherein the gate electrode is formed on top of a channel layer.

2. (original) The liquid crystal display device of claim 1, wherein the zero electric field is formed by nullifying an electric potential difference in the first liquid crystal layer.

3. (previously presented) The liquid crystal display device of claim 1, further comprising:

a common electrode formed on the first and the second color filter, wherein the first pixel electrode is arranged in a matrix shape and the zero electric field is formed by nullifying an electric potential difference between the common electrode and the first pixel electrode.

4. (withdrawn) The liquid crystal display device of claim 1, further comprising:

a plurality of thin film transistors formed on a third portion of the first substrate, the third portion corresponding to the first area;

a plurality of first pixel electrode being arranged in a matrix shape on a fourth portion of the first substrate, the fourth portion corresponding to the first area, the first pixel electrodes electrically disconnected to electrodes of the thin transistors to which an image signal is applied; and

a common electrode formed on the first and the second color filter, where in the zero electric field is formed by nullifying an electric potential difference in the first liquid crystal layer.

5. (original) The liquid crystal display device of claim 1, wherein the zero electric field is formed by forming an electrode layer on one of the first substrate and the second

substrate, the electrode layer making contact with the first liquid crystal layer.

6. (previously presented) The liquid crystal display device of claim 1, wherein a thickness of the first color filter is no less than a thickness of a pixel.

7. (original) The liquid crystal display device of claim 1, wherein the first color filter comprises three layers, and each of the three layers has a different thickness from each other.

8. (original) The liquid crystal display device of claim 7, wherein a thickness of the first color filter is regulated by controlling a coating thickness in a process in which the first color filter is coated on the second substrate or by a slit exposure process.

9. (withdrawn) The liquid crystal display device of claim 1, wherein the first color filter comprises three layers, and the three layers has a same thickness.

10. (withdrawn) The liquid crystal display device of claim 1, wherein the first color filter is a single layer.

11.-23. (canceled)